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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.

SERIAL NO.: 09/748,098

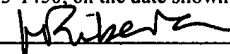
FILING DATE: December 21, 2000

TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching

EXAMINER: Charles A. Harkness

GROUP ART UNIT: 2183

ATTY. DKT. NO.: 20880-05093

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.	
Dated: Feb. 4, 2004	By: 
Hector J. Ribera, Reg. No. 54,397	

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AMENDMENT A AND RESPONSE UNDER 37 C.F.R. §1.111

Sir:

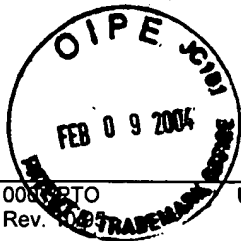
This amendment for the patent application identified above is in response to the Office Action dated November 4, 2003, which set a shortened statutory period for response that expires on February 4, 2003.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 9 of this paper.

Remarks begin on page 12 of this paper.

Kindly amend this application as indicated herein.



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0000 PTO Rev. 10-27-97		U.S. Department of Commerce Patent and Trademark Office		Application Number	09/748,098		
TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i>				Filing Date	December 21, 2000		
				First Named Inventor	Nicholas J. Kelsey		
				Group Art Unit Number	2183		
				Examiner Name	Charles A. Harkness		
Total Number of Pages in This Submission		20	Attorney Docket Number		20880-05093		

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SIGNATURE OF ATTORNEY OR AGENT		
Signature:		
Attorney/Reg. No.:	Hector J. Ribera, Reg. No. 54,397	Dated: Feb 4, 2004

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